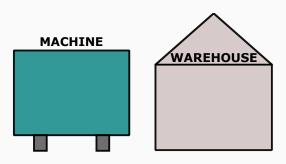
# Systems Design Laboratory

A Supervisory Control Example

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 $^2\mathsf{Department}$  of Computer Science, University of Verona, ITALY

# Machine-Warehouse Example



- A machine processing workpieces
- A warehouse storing finished workpieces

#### Machine



- Initially, the machine is IDLE
- Once it starts processing a workpiece it is BUSY
- Once it is BUSY it can finish processing a workpiece (this event must always be possible)
- The machine can process an infinite number of workpieces

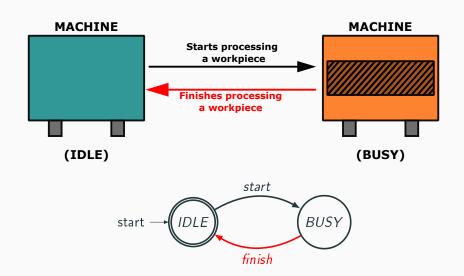
2

#### **Automaton for Machine**

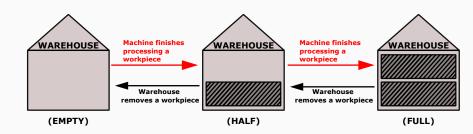


- States?
- Events and transitions?

#### **Automaton for Machine - States**



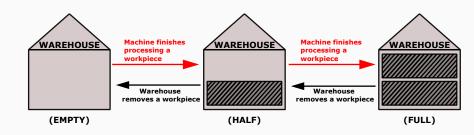
#### Warehouse



- The warehouse has a capacity of two workpieces
- Initially, the warehouse is EMPTY
- Synchronization: when the machine finishes processing a workpiece, the workpiece is stored in the warehouse
- At any time, the warehouse can remove a workpiece (if any)

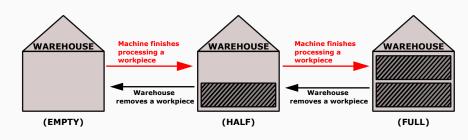
5

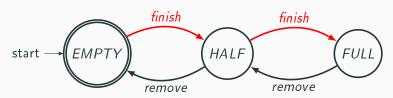
#### Warehouse



- States?
- Events and transitions?

#### Warehouse



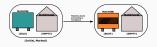


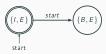
#### **Graphical Description**





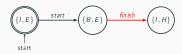
#### **Graphical Description**



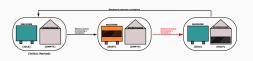


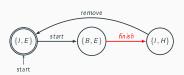
#### **Graphical Description**



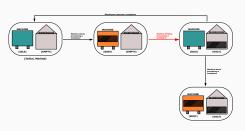


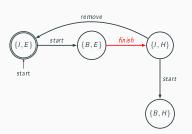
#### **Graphical Description**



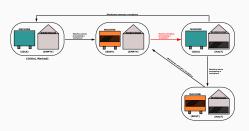


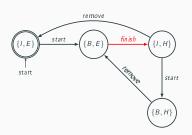
#### **Graphical Description**



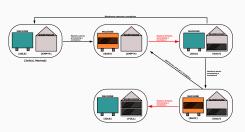


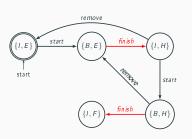
#### **Graphical Description**



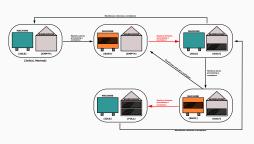


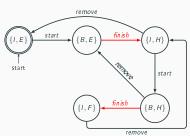
#### **Graphical Description**



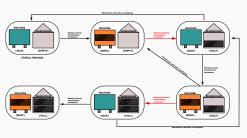


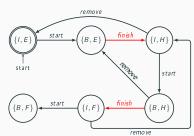
#### **Graphical Description**





#### **Graphical Description**

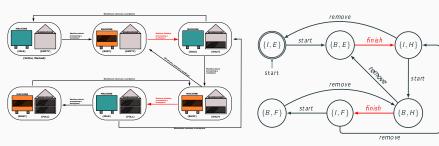




# Concurrency and Synchronization - Plant

# **Graphical Description**

# Parallel composition

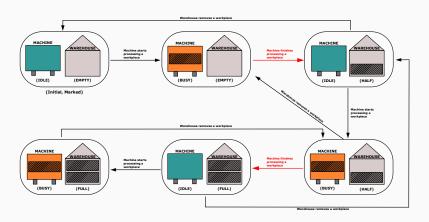


**Note:** When the machine is BUSY and the warehouse is FULL, despite *finish* is uncontrollable, the machine cannot execute it since *finish* is not executable by the warehouse.

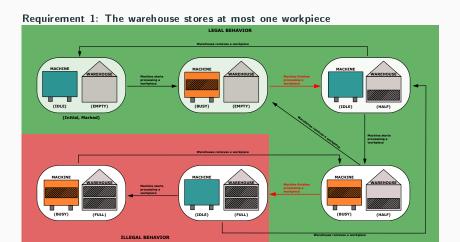
This means that, by exploiting synchronization, **and at plant level only**, we can prevent uncontrollable events from executing (by composing the "right" automata).

# **Example 1: Supervisory Control - Requirements**

#### Requirement 1: The warehouse stores at most one workpiece

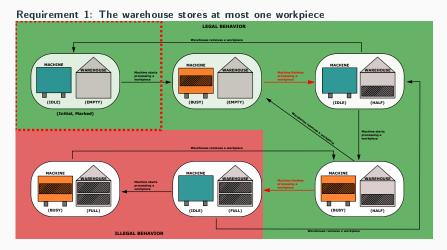


# Example 1: Supervisory Control - Desired Behavior



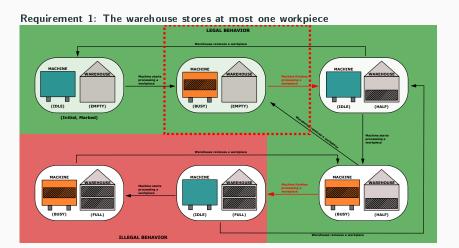
Can we control the plant in order to enforce such behavior? Can you spot any problem?

#### Example 1: Supervisory Control - Desired Behavior - Problem



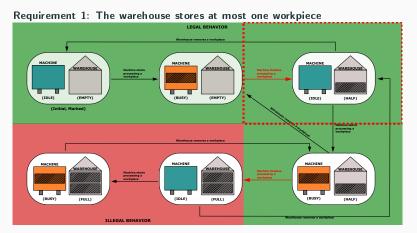
No problem. When the machine is IDLE and the warehouse is EMPTY, the machine can start processing a workpiece. This leads to machine BUSY and warehouse EMPTY which is still a desired behavior.

# Example 1: Supervisory Control - Desired Behavior - Problem



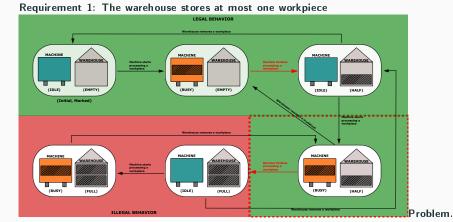
No problem. When the machine is BUSY and the warehouse is EMPTY, the machine can finish processing the workpiece. This leads to machine IDLE and warehouse HALF which is still a desired behavior.

#### Example 1: Supervisory Control - Desired Behavior - Problem



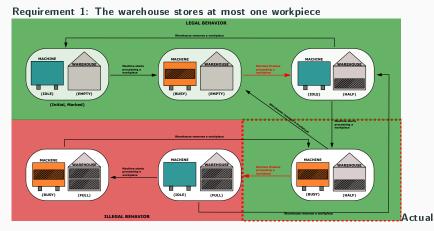
No problem. When the machine is IDLE and the warehouse is HALF, either the machine starts processing a workpiece (leading to machine BUSY, warehouse EMPTY) or the warehouse removes a workpiece from its storage (leading to machine IDLE, warehouse EMPTY). Either way, they are both desired behaviors.

# Example 1: Supervisory Control - Controllable Behavior



When the machine is BUSY and the warehouse is HALF, either the the warehouse removes a workpiece from its storage (leading to machine BUSY, warehouse EMPTY) or the machine finishes processing the workpiece (leading to machine IDLE, warehouse FULL). The second case is not a desired behavior.

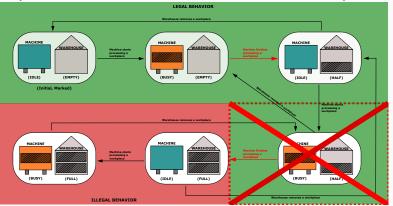
# Example 1: Supervisory Control - Desired Behavior



Problem. When the machine is BUSY and the warehouse is HALF we cannot prevent machine from finishing processing the workpiece because *finish* is uncontrollable and it is executable in the plant.

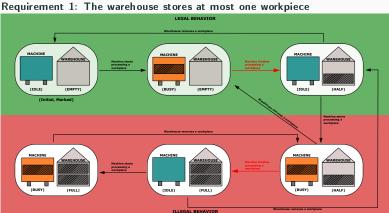
#### Example 1: Supervisory Control - Desired Behavior - Solution

Requirement 1: The warehouse stores at most one workpiece



Solution. We need to prevent the plant to get to that state. That is, we prevent machine to start processing a workpiece when machine is IDLE and the warehouse is HALF.

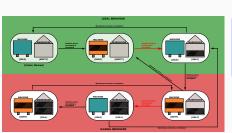
#### Example 1: Supervisory Control - Controllable Behavior



Actual controllable behavior (supremal controllable sublanguage).

#### **Example 1: Supervisory Control - Supervisor**

#### Requirement 1: The warehouse stores at most one workpiece



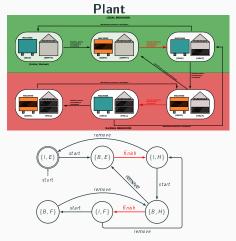
**Plant** 

#### Supervisor

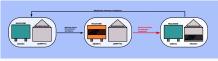


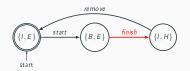
#### **Example 1: Supervisory Control - Supervisor**

Requirement 1: The warehouse stores at most one workpiece



# Supervisor





How can we synthesize this supervisor automatically?

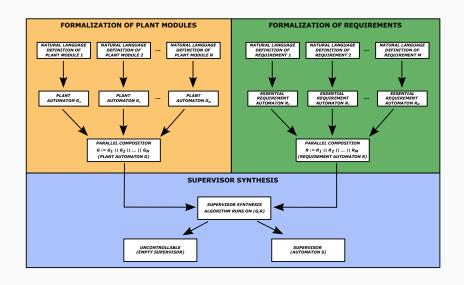
#### **Example 1: Supervisory Control - Supervisor**

#### Requirement 1: The warehouse stores at most one workpiece

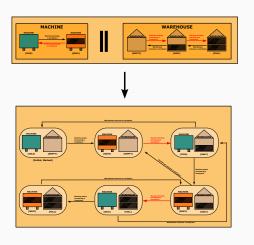
# Plant Supervisor

- Basically the supervisor here is the "intended part" of the system
- We would like to avoid computing it "by hand" (this case study is simple, but what about a real one?)

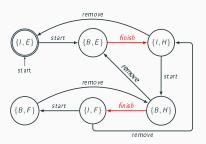
#### Supervisor Synthesis: Workflow



# Example 1: Supervisor Synthesis - Plant Formalization

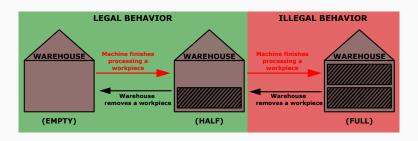


#### Plant Automaton G



# Example 1: Supervisor Synthesis - Essential Requirement

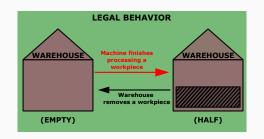
#### Requirement 1: The warehouse stores at most one workpiece



- States?
- Transitions?

#### Example 1: Supervisor Synthesis - Essential Requirement

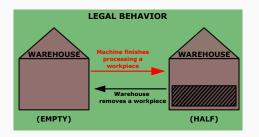
Requirement 1: The warehouse stores at most one workpiece

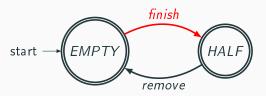




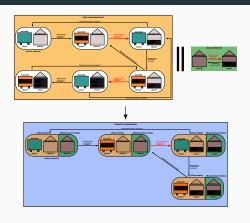
#### Example 1: Supervisor Synthesis - Essential Requirement

Requirement 1: The warehouse stores at most one workpiece





# **Example 1: Synthesis Algorithm - Tentative Supervisor**



# Plant Automaton G ((,,E) Start (B, E) Grad ((,,r)) ((g, E) Start ((,,F)) Grad ((g, R))

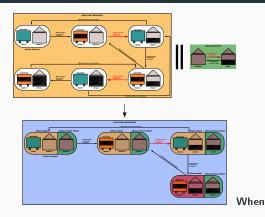


#### Parallel composition $G||R_1$



(Tentative Supervisor)

### Example 1: Synthesis Algorithm - Removal of States

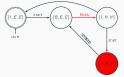


the machine is BUSY and the warehouse is HALF, the machine is prevented to finish. However, at plant level this is not permitted.

# Plant Automaton G (I, E) start (B, E) final (I, H) (B, F) start (I, F) final (B, H)

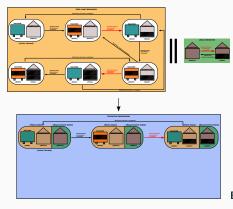
Requirement R<sub>1</sub>



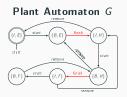


(Tentative Supervisor)

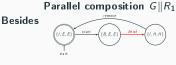
## Example 1: Synthesis Algorithm - No more removals



"uncontrollable states", we also need to remove non-accessible and non-coacessible states, if any (this example has none).



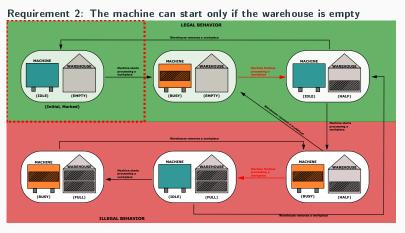
Requirement R<sub>1</sub>



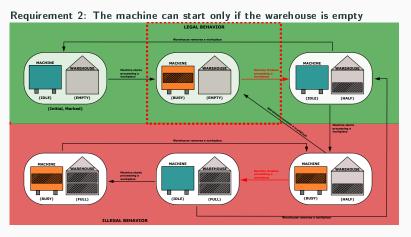
(Final Supervisor)

Requirement 2: The machine can start only if the warehouse is empty LEGAL BEHAVIOR MACHINE MACHINE MACHINE WAREHOUSE (EMPTY) (EMPTY) (IDLE) (IDLE) (HALF) (Initial, Marked) MACHINE MACHINE MACHINE (BUSY) (FULL) (BUSY) (HALF) ILLEGAL BEHAVIOR

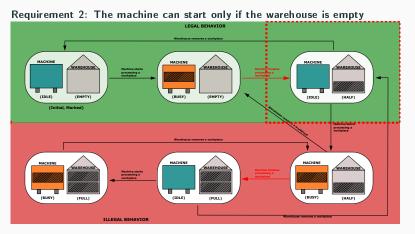
Let's see if we spot any problems



No problem. When the machine is IDLE and the warehouse is EMPTY, the machine can start processing a workpiece. This leads to machine BUSY and warehouse EMPTY which is still a desired behavior.

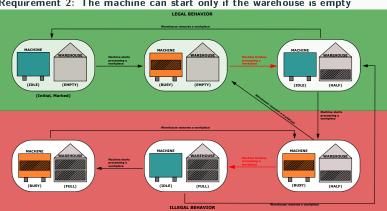


No problem. When the machine is BUSY and the warehouse is EMPTY, the machine can finish processing the workpiece. This leads to machine IDLE and warehouse HALF which is still a desired behavior.



No problem. When the machine is IDLE and the warehouse is HALF, either the warehouse removes a workpiece from its storage (leading to machine IDLE, warehouse EMPTY) or the machine starts processing a workpiece (leading to machine BUSY, warehouse EMPTY). The second is an undesired behavior.

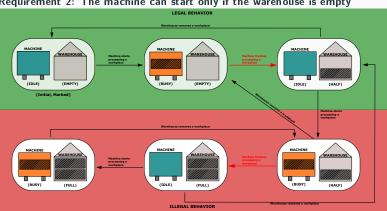
#### Example 2: Supervisory Control - Controllable Behavior



Requirement 2: The machine can start only if the warehouse is empty

However, in this state of the plant, since start is controllable, we can prevent the machine from starting working a workpiece. Therefore, the desired behavior is also controllable.

#### Example 2: Supervisory Control - Controllable Behavior



Requirement 2: The machine can start only if the warehouse is empty

However, in this state of the plant, since start is controllable, we can prevent the machine from starting working a workpiece. Therefore, the desired behavior is also controllable.

#### Example 2: Supervisor Synthesis - Essential Requirement

Requirement 2: The machine can start only if the warehouse is empty



**Essential Requirement** 

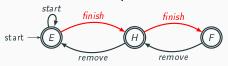
- States?
- Transitions?

#### Example 2: Supervisor Synthesis - Essential Requirement

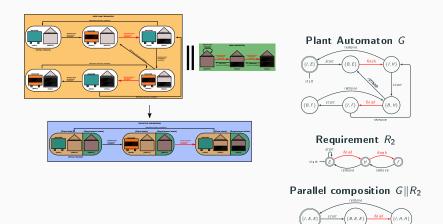
Requirement 2: The machine can start only if the warehouse is empty



#### **Essential Requirement**



#### **Example 2: Synthesis Algorithm - Tentative Supervisor**



(Tentative Supervisor, also final)

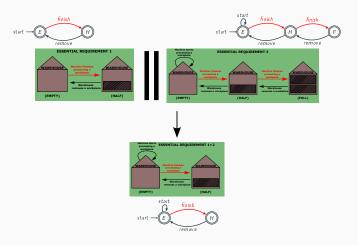
Requirement 1,2: The warehouse stores at most one workpiece AND the

machine can start only if the warehouse is empty LEGAL BEHAVIOR MACHINE MACHINE MACHINE (IDLE) (EMPTY) (EMPTY) (HALF) (IDLE) (Initial, Marked) MACHINE MACHINE (IDLE) (BUSY) (FULL) (FULL) (BUSY) ILLEGAL BEHAVIOR

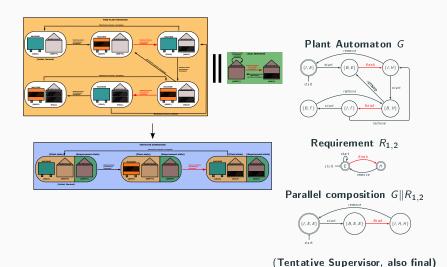
Same desired behavior of Requirement 2

#### Example 3: Combining Requirements 1 and 2

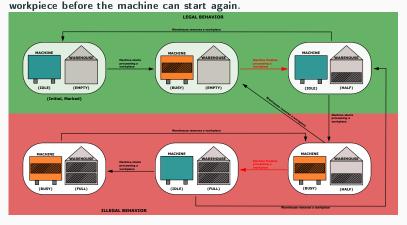
Requirement 1,2: The warehouse stores at most one workpiece AND the machine can start only if the warehouse is empty



#### **Example 3: Synthesis Algorithm - Tentative Supervisor**



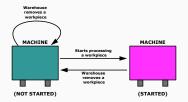
Requirement 4: If the Machine starts, then the warehouse must remove a



Same desired behavior of Requirement 2 and 1+2

#### Example 4: Supervisor Synthesis - Essential Requirement

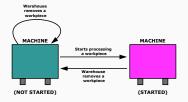
Requirement 4: If the Machine starts, then the warehouse must remove a workpiece before the machine can start again.



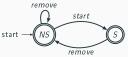
- States?
- Transitions?

#### Example 4: Supervisor Synthesis - Essential Requirement

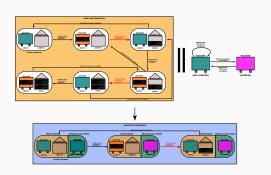
Requirement 4: If the Machine starts, then the warehouse must remove a workpiece before the machine can start again.

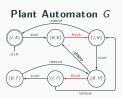


#### **Essential Requirement**



#### **Example 4: Synthesis Algorithm - Tentative Supervisor**





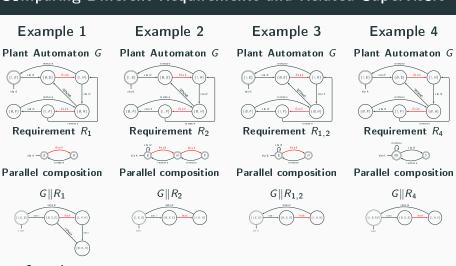


Parallel composition  $G||R_4$ 



(Tentative Supervisor, also final)

# Comparing Different Requirements and Related Supervisors





Supervisor





## Comparing Different Requirements and Related Supervisors

Example 1	Example 2	Example 3	Example 4	What we can say
(1, 2) 1911 (1, 12) (1, 13) 1911 (1, 13)			(E) 191 (E) 144 (E) 1911 (E) 1	Same plant G
111 - (E) (S) (E)		F.M	111 - 111	Syntactically Different
				Requirements
(1,1,1) 5 4 (1,1,1)	(1,1,1) 511 (1,1,1)	(1,7,7) (2,7,7) 511 (1,14,14)	(1,1,1) (1,1,1) (1,1,1)	R <sub>1</sub> is semantically dif-
			I	ferent from $R_2$ , $R_{1,2}$ ,
(E.H.H)				and $R_4$ , whereas $R_2$ ,
				$R_{1,2}$ , and $R_4$ are se-
				mantically equivalent.
((, r, r) 101 (0, r, r) 144 (1, H, H)	(1,E,E) (1,E,E) (1,H,H)	(1, E, E) 1911 (0, E, E) 1-14 (1, H, H)	(1, E, B 5) 1991 (B, E, 5) 1-14 (1, H, 5)	Same final supervisor
T 0				(same supremal con-
				trollable sublanguage)